

## AMENDMENTS TO THE CLAIMS

### **1-6. (Cancelled)**

7. (New) A video signal recording apparatus for digitally recording a first video signal containing information representing a copyrighted work and information representing a non-copyrighted work, comprising:

a copyright information detecting device configured to extract copyright information inserted in the first video signal;

an active pixel period detecting device configured to detect an active pixel period of the first video signal, and generate an active pixel period decision signal;

a video signal output device configured to output a second video signal containing information representing another non-copyrighted work;

a video signal generating device configured to generate a third video signal by replacing the first video signal by the second video signal in the active pixel period, upon determining, based on the copyright information and the active pixel period decision signal that the first video signal represents the information representing the copyrighted work;

a digital recording device configured to digitally record the third video signal on a recording medium as one video file;

a boundary detecting device configured to detect a boundary between the copyrighted work and the non-copyrighted work in the first video signal; and

a file structure information generating device configured to generate file structure information indicative of boundaries between the copyrighted work and the non-copyrighted work in the video file, wherein

the digital recording device is further configured to digitally record the file structure information.

8. (New) The video signal recording apparatus according to claim 7, wherein

the first video signal is an analog signal, and

said boundary detecting device comprises a clock configured to identify a period shorter than a frame period of the first video signal.

9. (New) The video signal recording apparatus according to claim 7, wherein  
the first video signal is a digital signal, and  
said boundary detecting device is further configured to generate an output based  
on program clock references included in the first video signal.